General Description

FSMOS® MOSFET is based on Oriental Semiconductor's unique device design to achieve low $R_{DS(ON)}$, low gate charge, fast switching and excellent avalanche characteristics. The high V_{th} series is specially designed to use in motor control systems with driving voltage of more than 10V.

Features

- Low $R_{DS(ON)}$ & FOM
- Extremely low switching loss
- Excellent reliability and uniformity
- Fast switching and soft recovery

RoHS compliant

Applications

- PD charger
- Motor driver
- Switching voltage regulator
- DC-DC convertor
- Switching mode power supply

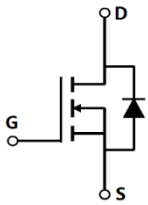
Key Performance Parameters

Parameter	Value	Unit
V_{DS}	100	V
$I_{D,\ pulse}$	160	A
$R_{DS(ON),\ max}$ @ V_{GS}=10V	20	mΩ
Q_g	19	nC

Marking Information

Product Name	Package	Marking
SFG10R20DNF	TO252	SFG10R20DN

Package & Pin information



Absolute Maximum Ratings at T_j=25°C unless otherwise noted

Parameter	Symbol	Value	Unit
Drain-source voltage	V_{DS}	100	V
Gate-source voltage	V_{GS}	±20	V
Continuous drain current[1], T_C=25 °C	I_D	40	A
Pulsed drain current[2], T_C=25 °C	$I_{D,\ pulse}$	160	A
Continuous diode forward current[1], T_C=25 °C	I_S	40	A
Diode pulsed current[2], T_C=25 °C	$I_{S,\ pulse}$	160	A
Power dissipation[3], T_C=25 °C	P_D	50	W
Single pulsed avalanche energy[5]	E_{AS}	54	mJ
Operation and storage temperature	T_{stg}, T_j	-55 to 150	°C

Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal resistance, junction-case	$R_{\theta JC}$	2.5	°C/W
Thermal resistance, junction-ambient[4]	$R_{\theta JA}$	62	°C/W

Electrical Characteristics at T_j=25°C unless otherwise specified

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Drain-source breakdown voltage	BV_{DSS}	100			V	V_{GS}=0 V, I_D=250 μA
Gate threshold voltage	$V_{GS(th)}$	2.5		4.0	V	V_{DS}=V_{GS}, I_D=250 μA
Drain-source on-state resistance	$R_{DS(ON)}$		15	20	mΩ	V_{GS}=10 V, I_D=10 A
Gate-source leakage current	I_{GSS}			100	nA	V_{GS}=20 V
Drain-source leakage current	I_{DSS}			1	μA	V_{DS}=100 V, V_{GS}=0 V
Gate resistance	R_G		3.4		Ω	f=1 MHz, Open drain

Dynamic Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Input capacitance	C_{iss}		1240		pF	V_{GS}=0 V, V_{DS}=25 V, f=100 kHz
Output capacitance	C_{oss}		606		pF	
Reverse transfer capacitance	C_{rss}		45		pF	
Turn-on delay time	$t_{d(on)}$		9.5		ns	V_{GS}=10 V, V_{DS}=50 V, R_G=2 Ω, I_D=40 A
Rise time	t_r		3.3		ns	
Turn-off delay time	$t_{d(off)}$		16.8		ns	
Fall time	t_f		3.1		ns	

Gate Charge Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Total gate charge	Q_g		18.9		nC	V_{GS}=10 V, V_{DS}=50 V, I_D=40 A
Gate-source charge	Q_{gs}		6.3		nC	
Gate-drain charge	Q_{gd}		4.9		nC	
Gate plateau voltage	$V_{plateau}$		5.7		V	

Body Diode Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Diode forward voltage	V_{SD}			1.3	V	I_S=10 A, V_{GS}=0 V
Reverse recovery time	t_{rr}		63.6		ns	V_R=50 V, I_S=40 A, di/dt=100 A/μs
Reverse recovery charge	Q_{rr}		71		nC	
Peak reverse recovery current	I_{rrm}		2.1		A	

Note

1) Calculated continuous current based on maximum allowable junction temperature.
2) Repetitive rating; pulse width limited by max. junction temperature.
3) Pd is based on max. junction temperature, using junction-case thermal resistance.
4) The value of $R_{\theta JA}$ is measured with the device mounted on 1 in^2 FR-4 board with 2oz. Copper, in a still air environment with T_a=25 °C.
5) V_{DD}=50 V,V_{GS}=10 V, L=0.3 mH, starting T_j=25 °C.

Electrical Characteristics Diagrams

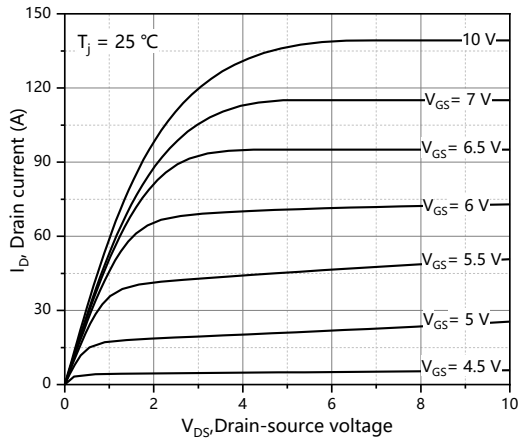


Figure 1. Typ. output characteristics

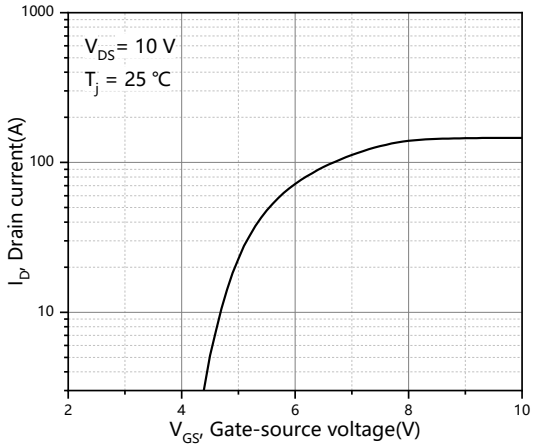


Figure 2. Typ. transfer characteristics

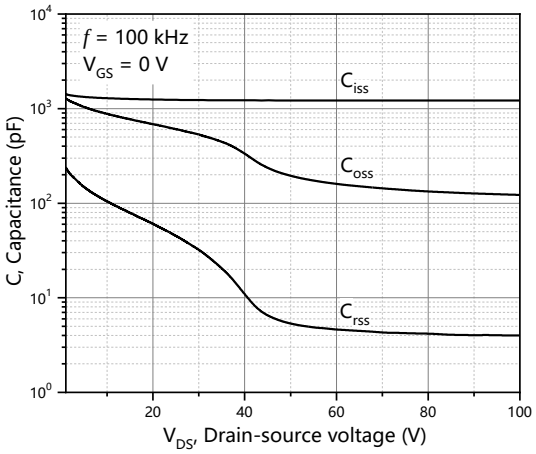


Figure 3. Typ. capacitances

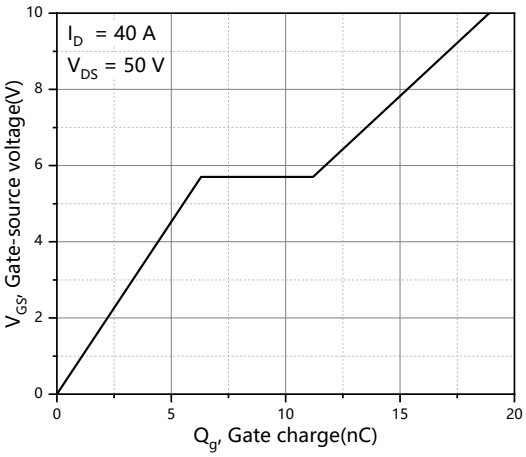


Figure 4. Typ. gate charge

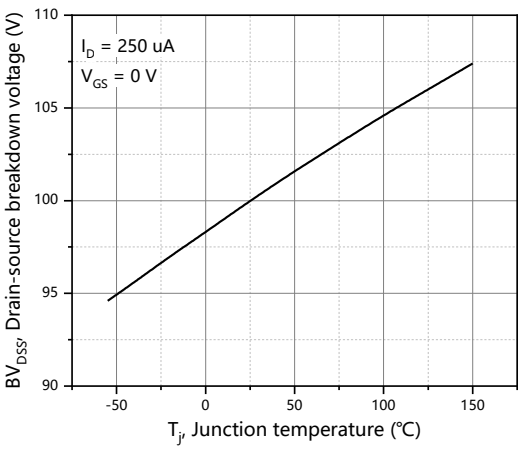


Figure 5. Drain-source breakdown voltage

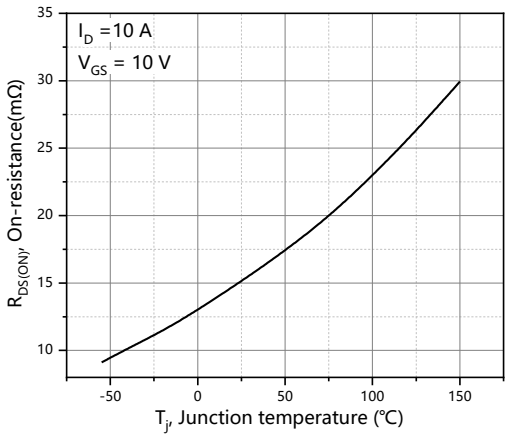


Figure 6. Drain-source on-state resistance

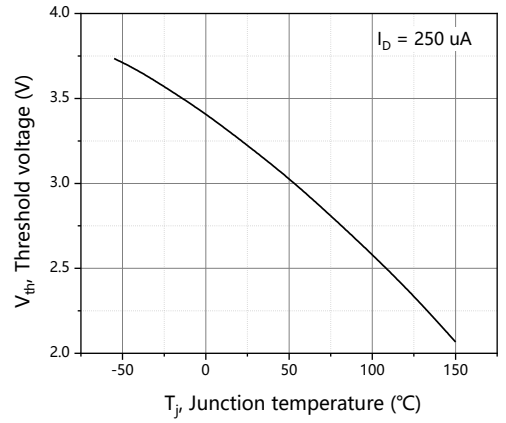


Figure 7. Threshold voltage

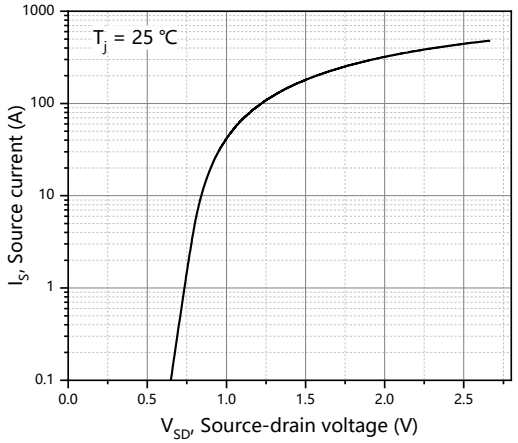


Figure 8. Forward characteristic of body diode

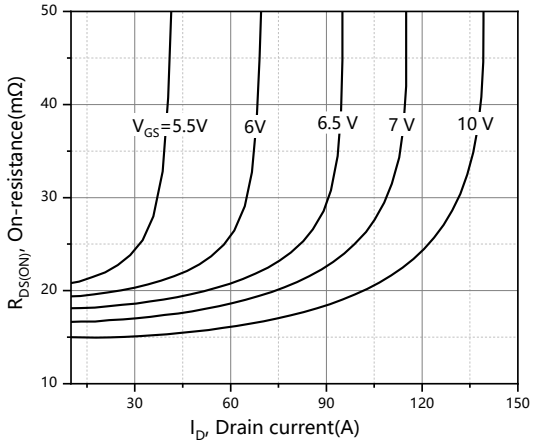


Figure 9. Drain-source on-state resistance

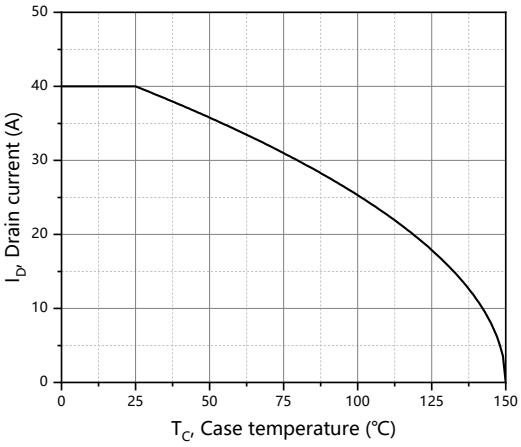


Figure 10. Drain current

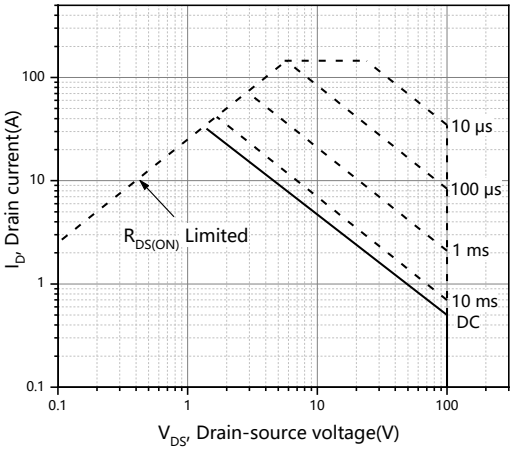


Figure 11. Safe operation area T_C=25℃

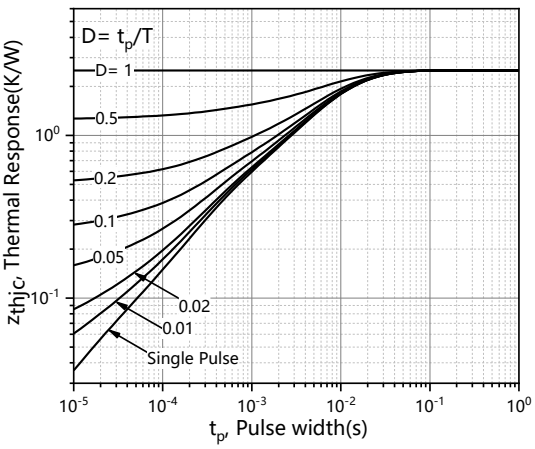


Figure 12. Max. transient thermal impedance

Test circuits and waveforms

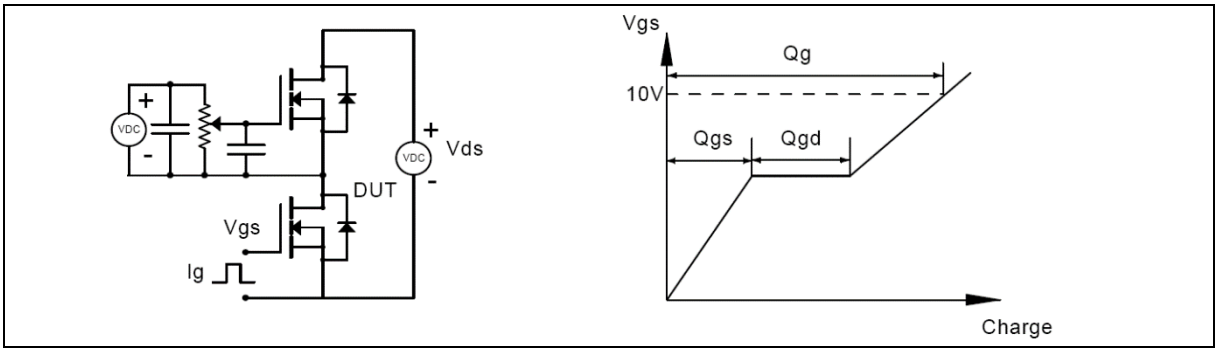


Figure 1. Gate charge test circuit & waveform

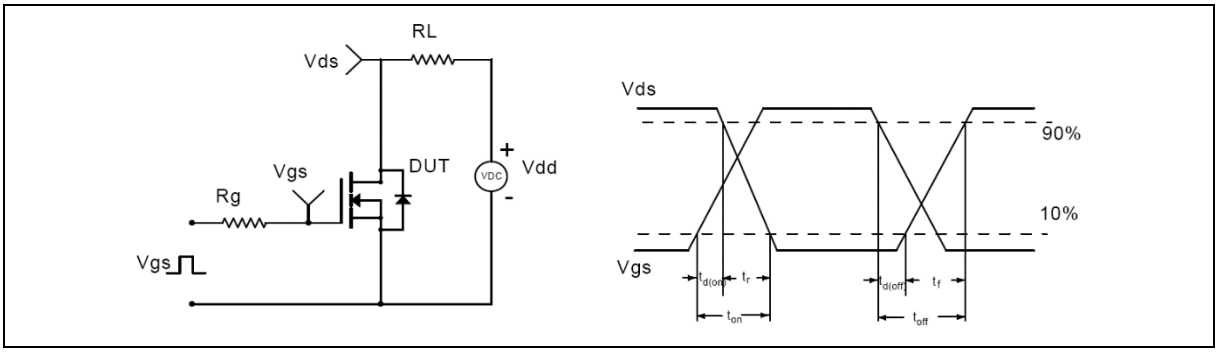


Figure 2. Switching time test circuit & waveforms

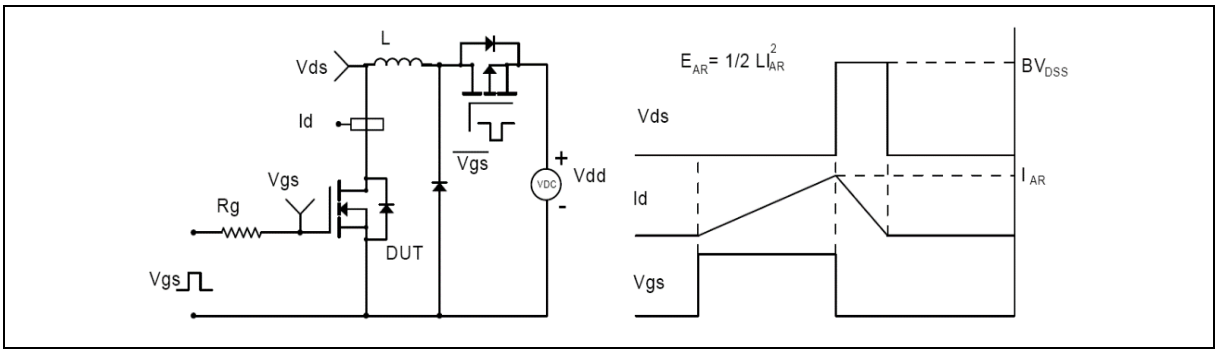


Figure 3. Unclamped inductive switching (UIS) test circuit & waveforms

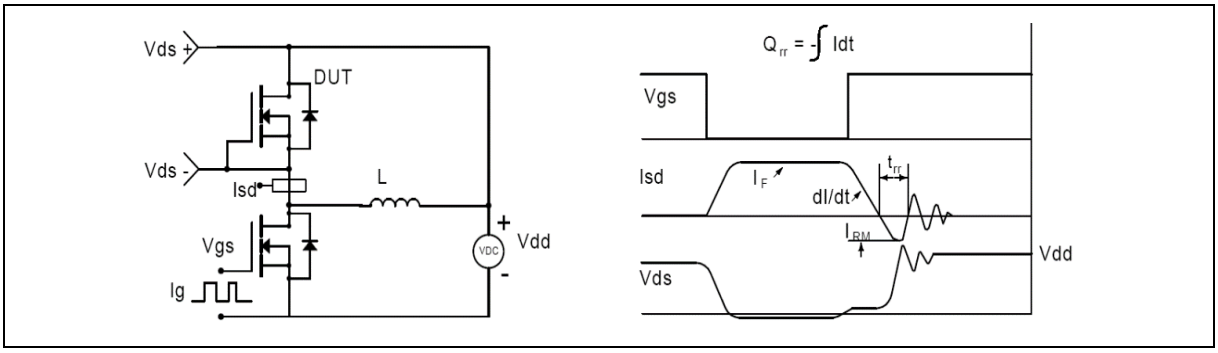
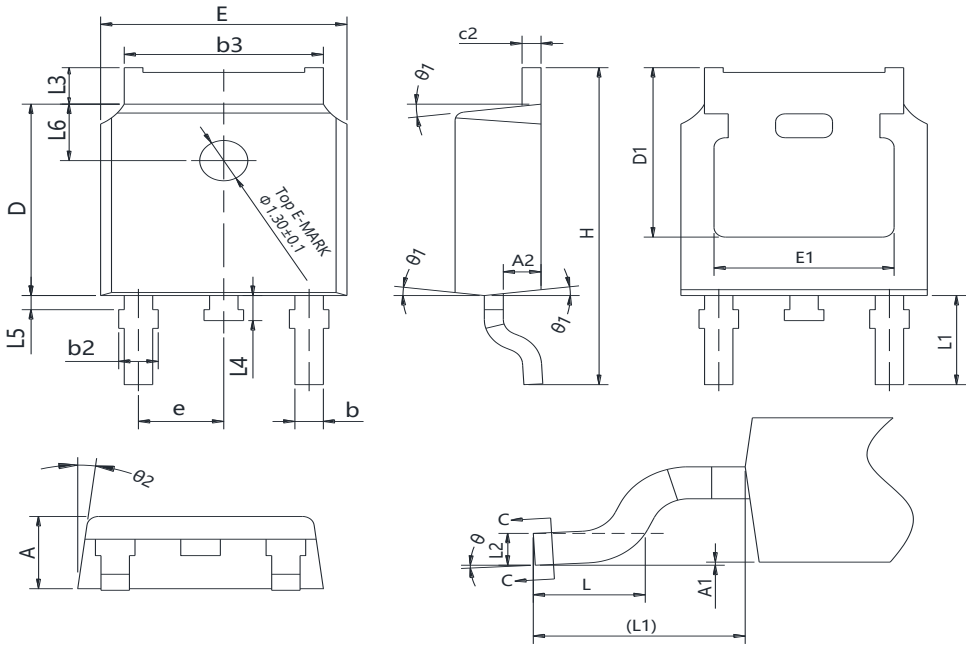


Figure 4. Diode reverse recovery test circuit & waveforms

Package Information



Symbol	mm		
	Min	Nom	Max
A	2.20	2.30	2.38
A1	0.00	-	0.10
A2	0.90	1.01	1.10
b	0.72	-	0.85
b2	0.72	-	0.90
b3	5.13	5.33	5.46
c2	0.47	-	0.60
D	6.00	6.10	6.20
D1	5.25	-	-
E	6.50	6.60	6.70
E1	4.70	-	-
e	2.186	2.286	2.386
H	9.80	10.10	10.40
L	1.40	1.50	1.70
L1	2.90 REF		
L2	0.508 BSC		
L3	0.90	-	1.25
L4	0.60	0.80	1.00
L5	0.15	-	0.75
L6	1.80 REF		
θ	0°	-	8°
θ1	5°	7°	9°
θ2	5°	7°	9°

Version 1: TO252-J package outline dimension

Ordering Information

Package Type	Units/ Reel	Reels/ Inner Box	Units/ Inner Box	Inner Boxes/ Carton Box	Units/ Carton Box
TO252-J	2500	2	5000	5	25000

Product Information

Product	Package	Pb Free	RoHS	Halogen Free
SFG10R20DNF	TO252	yes	yes	yes

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