

General Description

The GreenMOS[®] high voltage MOSFET utilizes charge balance technology to achieve outstanding low on-resistance and lower gate charge. It is engineered to minimize conduction loss, provide superior switching performance and robust avalanche capability.

The GreenMOS[®] Generic series is optimized for extreme switching performance to minimize switching loss. It is tailored for high power density applications to meet the highest efficiency standards.

Features

- Low $R_{DS(ON)}$ & FOM
- Extremely low switching loss
- Excellent stability and uniformity

GreenMOS[®]



Applications

- PC power
- LED lighting
- Telecom power
- Server power
- EV Charger
- Solar/UPS

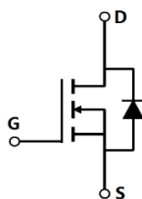
Key Performance Parameters

Parameter	Value	Unit
V_{DS}	900	V
I_D , pulse	15	A
$R_{DS(ON)}$, max @ $V_{GS}=10V$	1.2	Ω
Q_g	12.5	nC

Marking Information

Product Name	Package	Marking
OSG90R1K2IF	TO262	OSG90R1K2I

Package & Pin Information



Absolute Maximum Ratings at $T_j=25^\circ\text{C}$ unless otherwise noted

Parameter	Symbol	Value	Unit
Drain-source voltage	V_{DS}	900	V
Gate-source voltage	V_{GS}	± 30	V
Continuous drain current ¹⁾ , $T_C=25^\circ\text{C}$	I_D	5	A
Continuous drain current ¹⁾ , $T_C=100^\circ\text{C}$		3.2	
Pulsed drain current ²⁾ , $T_C=25^\circ\text{C}$	$I_{D, pulse}$	15	A
Continuous diode forward current ¹⁾ , $T_C=25^\circ\text{C}$	I_S	5	A
Diode pulsed current ²⁾ , $T_C=25^\circ\text{C}$	$I_{S, pulse}$	15	A
Power dissipation ³⁾ , $T_C=25^\circ\text{C}$	P_D	83	W
Single pulsed avalanche energy ⁴⁾	E_{AS}	160	mJ
MOSFET dv/dt ruggedness, $V_{DS}=0\dots 480\text{ V}$	dv/dt	50	V/ns
Reverse diode dv/dt, $V_{DS}=0\dots 480\text{ V}$, $I_{SD}\leq I_D$	dv/dt	15	V/ns
Operation and storage temperature	T_{stg}, T_j	-55 to 150	$^\circ\text{C}$

Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal resistance, junction-case	$R_{\theta JC}$	1.5	$^\circ\text{C/W}$
Thermal resistance, junction-ambient	$R_{\theta JA}$	62	$^\circ\text{C/W}$

Electrical Characteristics at $T_j=25^\circ\text{C}$ unless otherwise specified

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Drain-source breakdown voltage	BV_{DSS}	900			V	$V_{GS}=0\text{ V}$, $I_D=250\ \mu\text{A}$
Gate threshold voltage	$V_{GS(th)}$	2.0		4.0	V	$V_{DS}=V_{GS}$, $I_D=250\ \mu\text{A}$
Drain-source on-state resistance	$R_{DS(ON)}$		1.0	1.2	Ω	$V_{GS}=10\text{ V}$, $I_D=2\text{ A}$
			2.88			$V_{GS}=10\text{ V}$, $I_D=2\text{ A}$, $T_j=150^\circ\text{C}$
Gate-source leakage current	I_{GSS}			100	nA	$V_{GS}=30\text{ V}$
				-100		$V_{GS}=-30\text{ V}$
Drain-source leakage current	I_{DSS}			10	μA	$V_{DS}=900\text{ V}$, $V_{GS}=0\text{ V}$
Gate resistance	R_G		29		Ω	$f=1\text{ MHz}$, Open drain

Dynamic Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Input capacitance	C_{iss}		874		pF	$V_{GS}=0\text{ V}$, $V_{DS}=50\text{ V}$, $f=100\text{ kHz}$
Output capacitance	C_{oss}		37.5		pF	
Reverse transfer capacitance	C_{rss}		1.7		pF	
Turn-on delay time	$t_{d(on)}$		33.2		ns	$V_{GS}=10\text{ V}$, $V_{DS}=400\text{ V}$, $R_G=33\ \Omega$, $I_D=5\text{ A}$
Rise time	t_r		26.5		ns	
Turn-off delay time	$t_{d(off)}$		44		ns	
Fall time	t_f		17.6		ns	

Gate Charge Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Total gate charge	Q_g		12.5		nC	$V_{GS}=10\text{ V}$, $V_{DS}=400\text{ V}$, $I_D=5\text{ A}$
Gate-source charge	Q_{gs}		3.8		nC	
Gate-drain charge	Q_{gd}		4.3		nC	
Gate plateau voltage	$V_{plateau}$		5.8		V	

Body Diode Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Diode forward voltage	V_{SD}			1.3	V	$I_S=5\text{ A}$, $V_{GS}=0\text{ V}$
Reverse recovery time	t_{rr}		265.9		ns	$V_R=400\text{ V}$, $I_S=5\text{ A}$, $di/dt=100\text{ A}/\mu\text{s}$
Reverse recovery charge	Q_{rr}		2.9		μC	
Peak reverse recovery current	I_{rrm}		19.5		A	

Note

- 1) Calculated continuous current based on maximum allowable junction temperature.
- 2) Repetitive rating; pulse width limited by max. junction temperature.
- 3) P_d is based on max. junction temperature, using junction-case thermal resistance.
- 4) $V_{DD}=100\text{ V}$, $V_{GS}=10\text{ V}$, $L=80\text{ mH}$, starting $T_j=25\text{ }^\circ\text{C}$.

Electrical Characteristics Diagrams

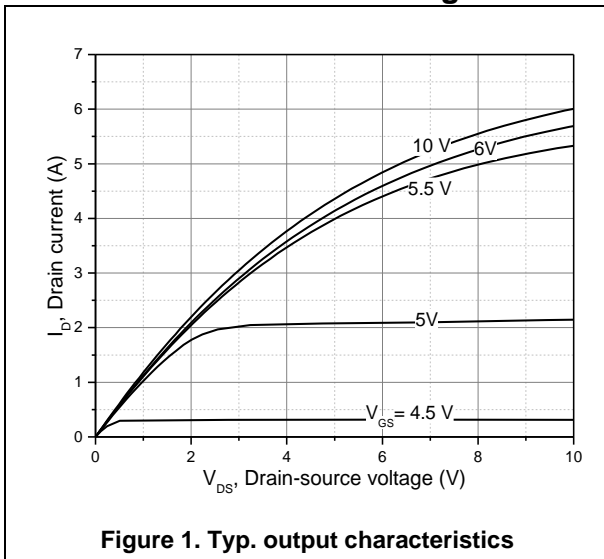


Figure 1. Typ. output characteristics

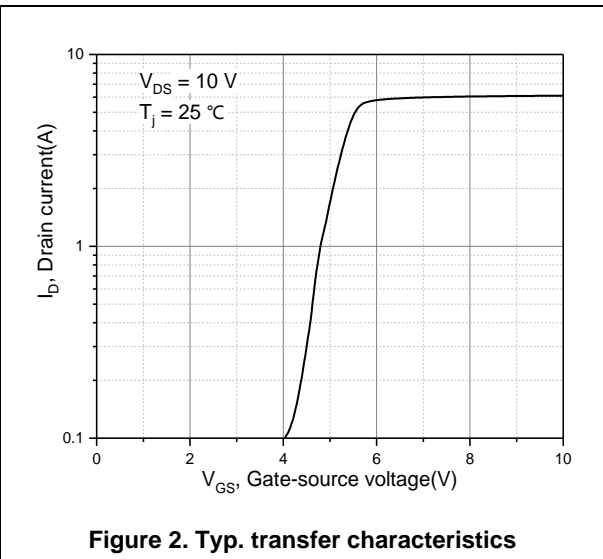


Figure 2. Typ. transfer characteristics

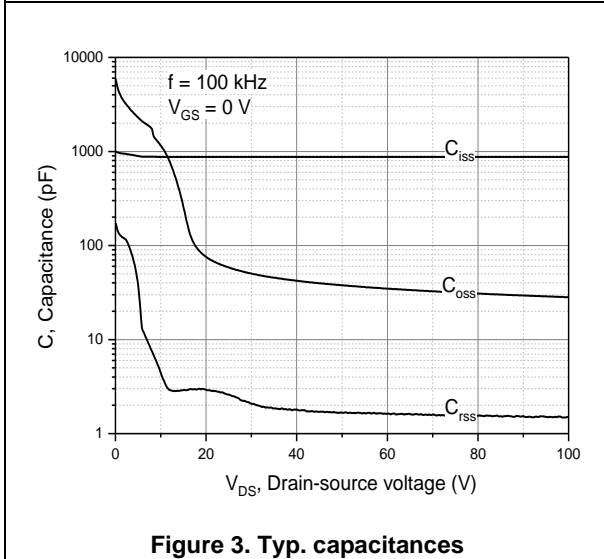


Figure 3. Typ. capacitances

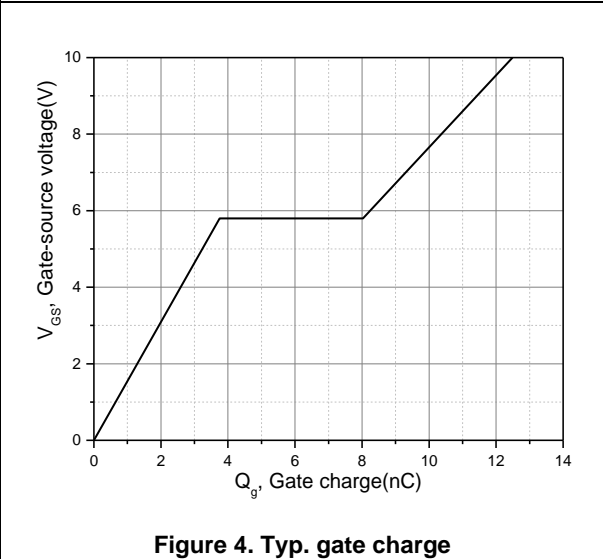


Figure 4. Typ. gate charge

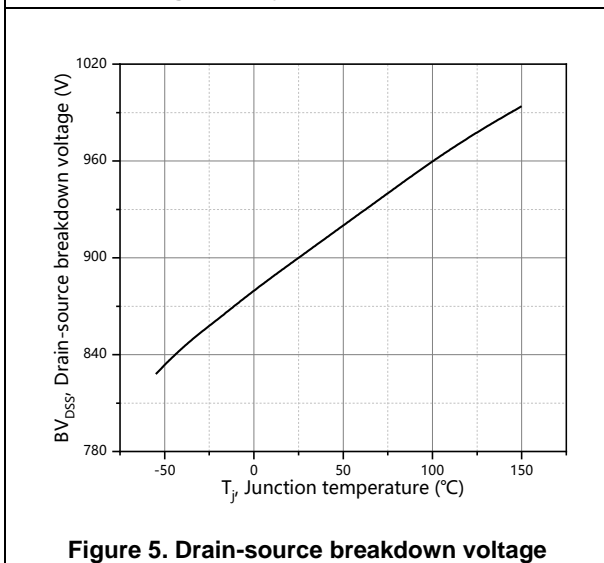


Figure 5. Drain-source breakdown voltage

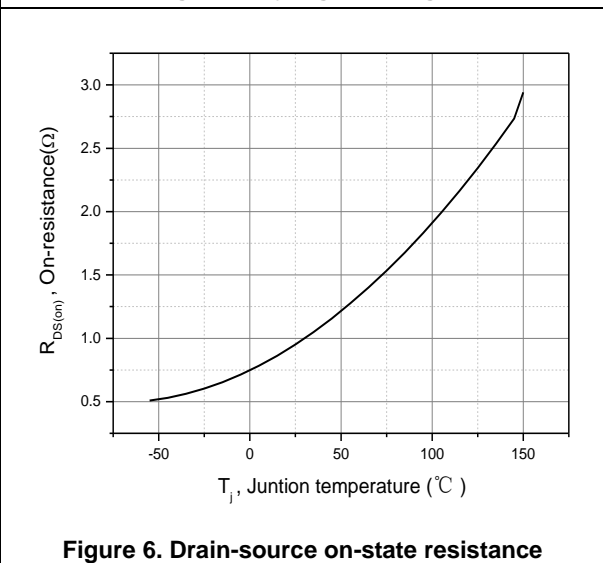
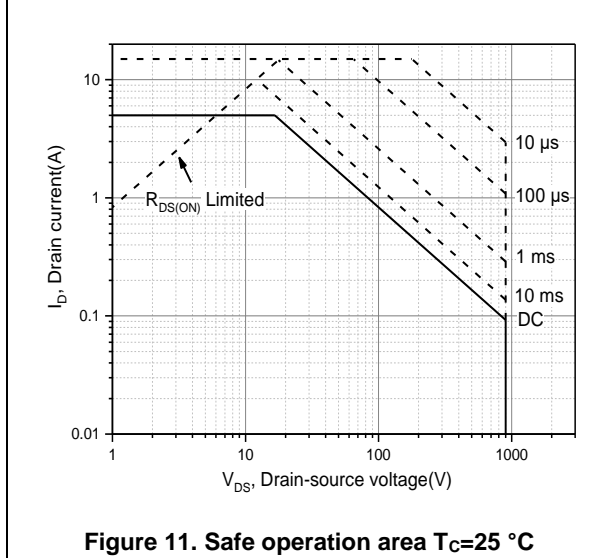
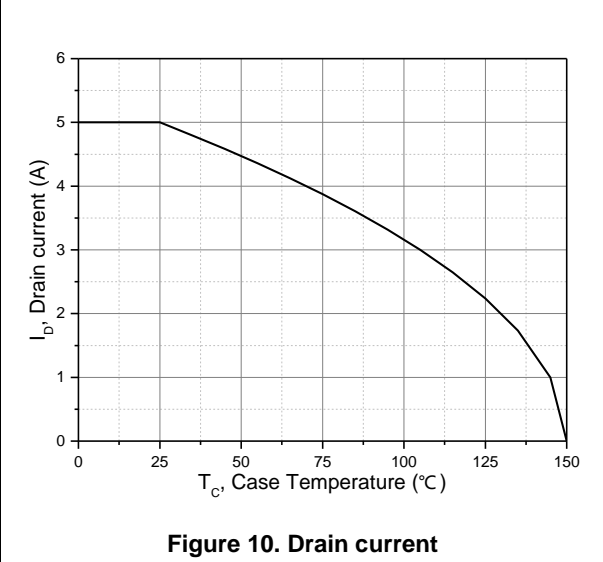
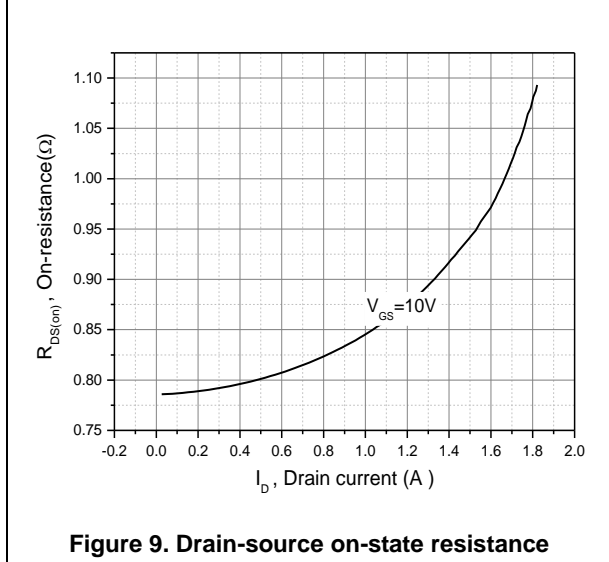
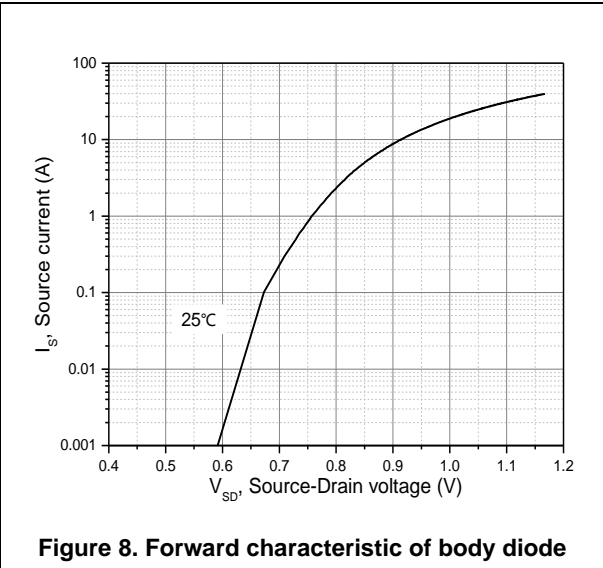
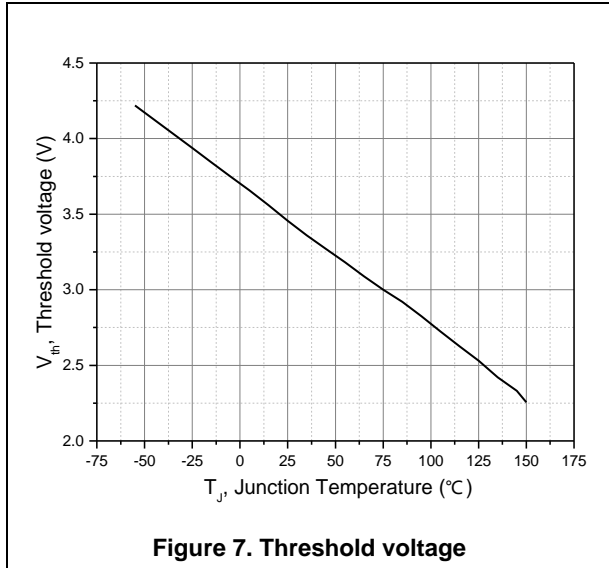


Figure 6. Drain-source on-state resistance



Test circuits and waveforms



Figure 1. Gate charge test circuit & waveform

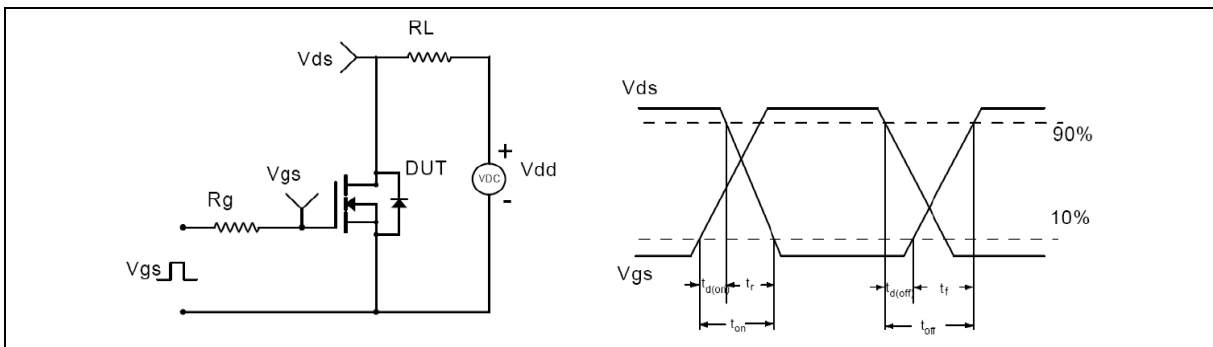


Figure 2. Switching time test circuit & waveforms

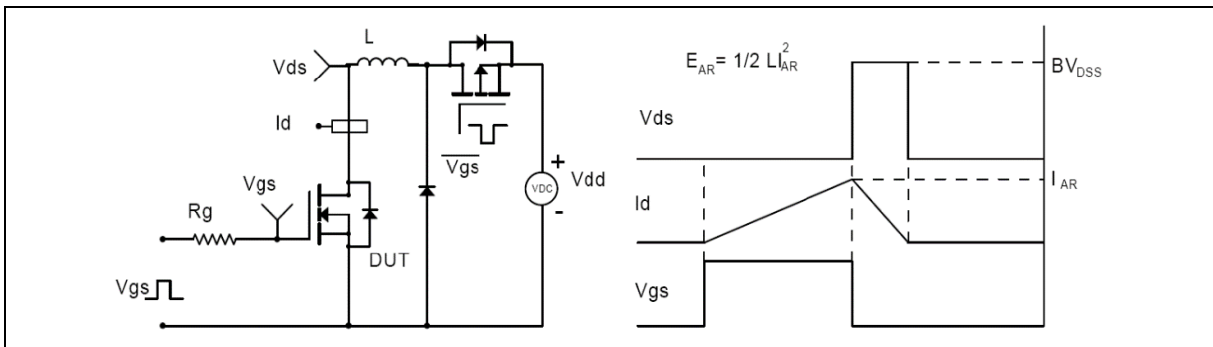
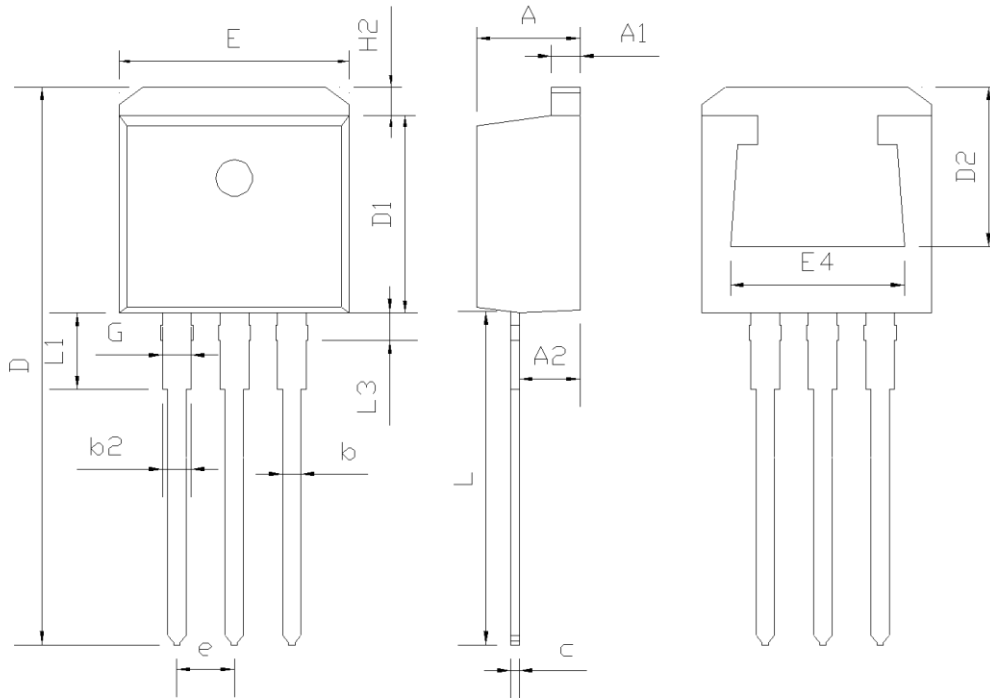


Figure 3. Unclamped inductive switching (UIS) test circuit & waveforms



Figure 4. Diode reverse recovery test circuit & waveforms

Package Information



Symbol	mm		
	Min	Nom	Max
A	4.37	4.57	4.77
A1	1.22	1.27	1.42
A2	2.49	2.69	2.89
b	0.71	0.81	0.96
b2	1.17	1.27	1.42
c	0.28	0.38	0.53
D	23.20	23.70	24.02
D1	8.50	8.70	8.90
D2	6.00	-	-
E	9.86	10.16	10.36
E4	7.06	-	-
e	2.54BSC		
H2	-	-	1.50
L	13.33	13.73	14.13
L1	3.50	3.75	4.00
L3	1.28	1.43	1.58
G	1.25	1.35	1.50

Version 1: TO262-P package outline dimension

Ordering Information

Package Type	Units/ Tube	Tubes/ Inner Box	Units/ Inner Box	Inner Boxes/ Carton Box	Units/ Carton Box
TO262-P	50	20	1000	6	6000

Product Information

Product	Package	Pb Free	RoHS	Halogen Free
OSG90R1K2IF	TO262	yes	yes	yes

Legal Disclaimer

The information given in this document shall in no event be regarded as a guarantee of conditions or characteristics. With respect to any examples or hints given herein, any typical values stated herein and/or any information regarding the application of the device, Oriental Semiconductor hereby disclaims any and all warranties and liabilities of any kind, including without limitation, warranties of non-infringement of intellectual property rights of any third party.

For further information on technology, delivery terms and conditions and prices, please contact the Oriental Semiconductor sales representatives (www.orientalsemi.com).

© Oriental Semiconductor Co.,Ltd. All Rights Reserved 